	,			
L Number	Hits	Search Text	DB	Time stamp
-	1	CPU adj code adj latency	USPAT;	2003/12/02
			US-PGPUB;	10:10
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	76	CPU adj latency	USPAT;	2003/12/02
			US-PGPUB;	10:11
			EPO; JPO;	
			DERWENT;	
<u> </u>	453	latency.ti.	IBM_TDB USPAT	2003/12/02
_	433	Tatency.tr.	USPAI	15:50
_	396	register same (processor or decoder) same	USPAT	2003/12/02
	3,0	(CAM or (content adj addressable adj	OSTAT	16:09
		memory))		10.03
-	1	6392570.pn.	USPAT	2003/12/02
	_			15:54
-	48	((simultaneous\$2 or "same time" or	USPAT	2003/12/02
		concurrent\$2) with (transmi\$5 or		16:42
		transfer\$3 or send\$3 or reciev\$3) with		
		(data or information or instructions))		
		same (processor or CPU) with (CAM or		
i		(content adj addressable adj memory) or		
		memory or storage or register) same		
	_	(valid\$3 or (error adj check\$3))		
-	10	((simultaneous\$2 or "same time" or	USPAT	2003/12/02
		concurrent\$2) with ((transmi\$5 or		16:41
		transfer\$3 or send\$3 or reciev\$3) adj		
		(data or information or instructions)))		
		same (processor or CPU) with (CAM or		
		(content adj addressable adj memory) or		
		memory or storage or register) same (valid\$3 or (error adj check\$3))		
_	8	((simultaneous\$2 or "same time" or	USPAT	2003/12/02
		concurrent\$2) with ((transmi\$5 or	22171	16:44
	,	transfer\$3 or send\$3 or reciev\$3) adj		
		(data or information or instructions)))		
	:	with (processor or CPU) with (CAM or		
		(content adj addressable adj memory) or		
		memory or storage or register) same		
		(valid\$3 or (error adj check\$3))		
-	288	(simultaneous\$2 or "same time" or	USPAT	2003/12/04
		concurrent\$2) with ((transmi\$5 or		09:02
		transfer\$3 or send\$3 or reciev\$3) adj		
		(data or information or instructions))		
1		with (processor or CPU) with (CAM or		
]		(content adj addressable adj memory) or		
 _	29	memory or storage or register)	USPAT	2002/12/02
_	29	714/\$.ccls. and (simultaneous\$2 or "same time" or concurrent\$2) with ((transmi\$5	USPAT	2003/12/03
		or transfer\$3 or send\$3 or reciev\$3) adj		09:07
		(data or information or instructions))		
		with (processor or CPU) with (CAM or		
		(content adj addressable adj memory) or		
		memory or storage or register)		
-	2142	(register) with (transfer\$4) with (CPU or	USPAT	2003/12/03
	-	processor) with memory		13:12
-	376	((register) adj2 (transfer\$4)) with (CPU	USPAT	2003/12/03
		or processor) with memory		13:11
-	69	(USPAT	2003/12/03
		processor) with memory with ("same time"		13:58
		or simultaneously)		
-	9	(USPAT	2003/12/03
		or processor) with memory with ("same		13:12
		time" or simultaneously)		

- 60	((register) with (transfer\$4) with (CPU or processor) with memory with ("same time" or simultaneously)) not	USPAT	2003/12/03 15:54
	<pre>(((register) adj2 (transfer\$4)) with (CPU or processor) with memory with ("same time" or simultaneously))</pre>		
- 18	crossroads.as.	USPAT	2003/12/04
48	<pre>((transmit\$4 or send\$4 or send\$3) with (simultaneous\$2 or "same time" or parallel) with (memory or register)) same (error or fault or failure)</pre>	USPAT	2003/12/05 12:06
24		USPAT	2003/12/03 16:23
-	((simultaneous\$2 or "same time" or parallel) adj transactions) with CAM	USPAT	2003/12/03 16:24
- 208		USPAT	2003/12/03
-	((simultaneous\$2 or "same time") adj transactions) with CPU with memory	USPAT	2003/12/04
- 44		USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/04 08:51
- 5	latency same (content adj addressable adj memory) same CPU	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/04 08:57
_ 223	710/20,21.ccls. and (error or fault or failure)	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2003/12/05 08:47
- 150	710/20,21.ccls. and (error or fault or failure) and register	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/04 08:58
- 69	710/20,21.ccls. and (error or fault or failure) same register	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/04 09:01
94	(710/\$.ccls. or 714/\$.ccls.) and (simultaneous\$2 or "same time" or concurrent\$2) with ((transmi\$5 or transfer\$3 or send\$3 or reciev\$3) adj (data or information or instructions)) with (processor or CPU) with (CAM or (content adj addressable adj memory) or memory or storage or register)	USPĀT	2003/12/04 09:03
- 13		USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2003/12/04 09:03
- 1315	fault or failure)	USPAT	2003/12/04 14:40
679	failure)	USPAT	2003/12/04 14:41
- 800	(simultaneously or "same time")	USPAT	2003/12/04 14:41
570	714/718.ccls. and (simultaneously or "same time")	USPAT	2003/12/04 14:41
87	(simultaneously or "same time") with CPU	USPAT	2003/12/04 14:41
- 15	714/718.ccls. and (simultaneously or "same time") with CPU	USPAT	2003/12/04 14:48

Search History 12/5/03 5:38:37 PM Page 2

-	2	714/\$.ccls. and ((simultaneously or "same	USPAT	2003/12/04
		time") with CPU) same (error adj		14:51
_	37	correction adj code) 714/\$.ccls. and ((simultaneously or "same	USPAT	2003/12/04
]	time") with CPU) same (error adj fault or	OSFAI	14:51
		failure)		
-	11	bucht.in.	USPAT;	2003/12/05
			US-PGPUB;	10:01
			EPO; JPO;	
			DERWENT; IBM TDB	
_	2	6256708.pn.	USPAT;	2003/12/05
i		•	US-PGPUB;	12:05
			EPO; JPO;	
			DERWENT;	
_	357	711/\$.ccls. and memory same valid\$3 same	IBM_TDB USPAT;	2003/12/05
	337	simultaneously	US-PGPUB;	12:36
		3-1111-1-1111-1111-1111-1111-1111-1111-1111	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	149	1 , ,	USPAT	2003/12/05
		(simultaneous\$2 or "same time" or parallel) with (memory or register)) same		12:07
	•	(valid\$3)		
-	37	((transmit\$4 or send\$4 or send\$3) with	USPAT	2003/12/05
		(simultaneous\$2 or "same time" or		15:02
		parallel) with (memory or register) with		
_	80	(CPU or processor)) same (valid\$3) ((transmit\$4 or send\$4 or send\$3) with	USPAT	2003/12/05
		(simultaneous\$2 or "same time" or	OSPAI	12:18
		parallel) with (memory or register) with	•	
		(CPU or processor)) same (valid\$3 or		
	72	error or fault) 711/8,144.ccls. and valid\$3 same	USPAT;	2003/12/05
	'2	simultaneously	US-PGPUB;	15:04
			EPO; JPO;	10.01
			DERWENT;	
		6000400	IBM_TDB	2002/12/25
_	2	6289420.pn.	USPAT;	2003/12/05 14:19
	1		US-PGPUB; EPO; JPO;	17.17
			DERWENT;	
			IBM_TDB	
-	50	711/8,144.ccls. and ((transmit\$3 or	USPAT;	2003/12/05
		signal\$3) adj memory)	US-PGPUB; EPO; JPO;	15:07
			DERWENT;	
		*	IBM_TDB	
-	0		USPAT;	2003/12/05
		signal\$3) adj memory) same valid\$3 same	US-PGPUB;	15:07
		simultaneous\$2	EPO; JPO; DERWENT;	
			IBM TDB	
-	8	((USPAT;	2003/12/05
		signal\$3) adj memory) same valid\$3	US-PGPUB;	15:07
			EPO; JPO;	
			DERWENT; IBM TDB	
	L	<u> </u>	IDN IDD	<u> </u>